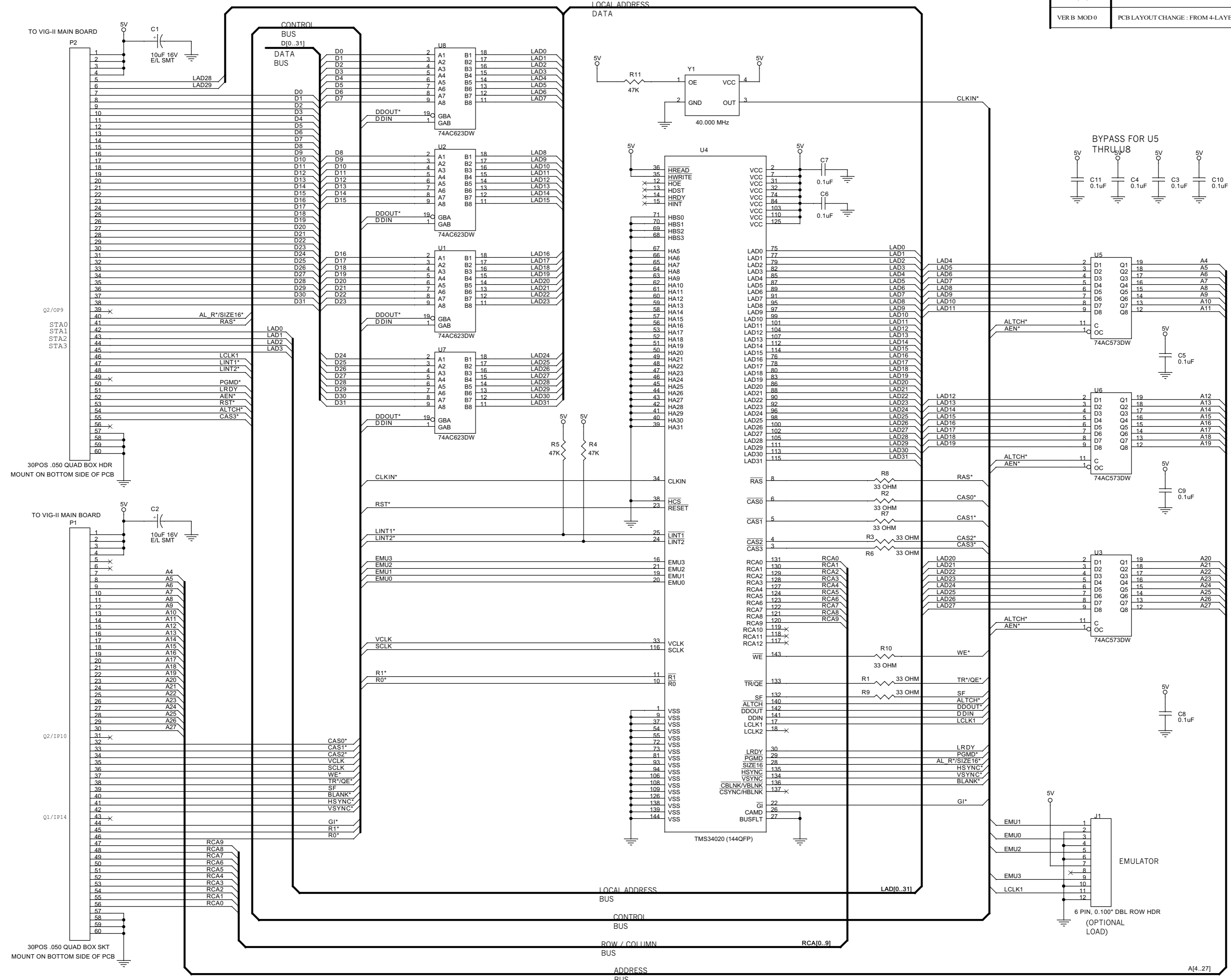


REVISION	DESCRIPTION	DATE	APPROVAL
VER A (1.0) MOD 0	ENGINEERING PRODUCT RELEASE	08/04/97	J.B.S.
VER B MOD 0	PCB LAYOUT CHANGE : FROM 4-LAYER TO 6-LAYER	01/28/98	J.B.S.



Prepared		Casino Data Systems 3300 Birtcher Drive Las Vegas, NV 89118 (702) 269-5000	
Checked			
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